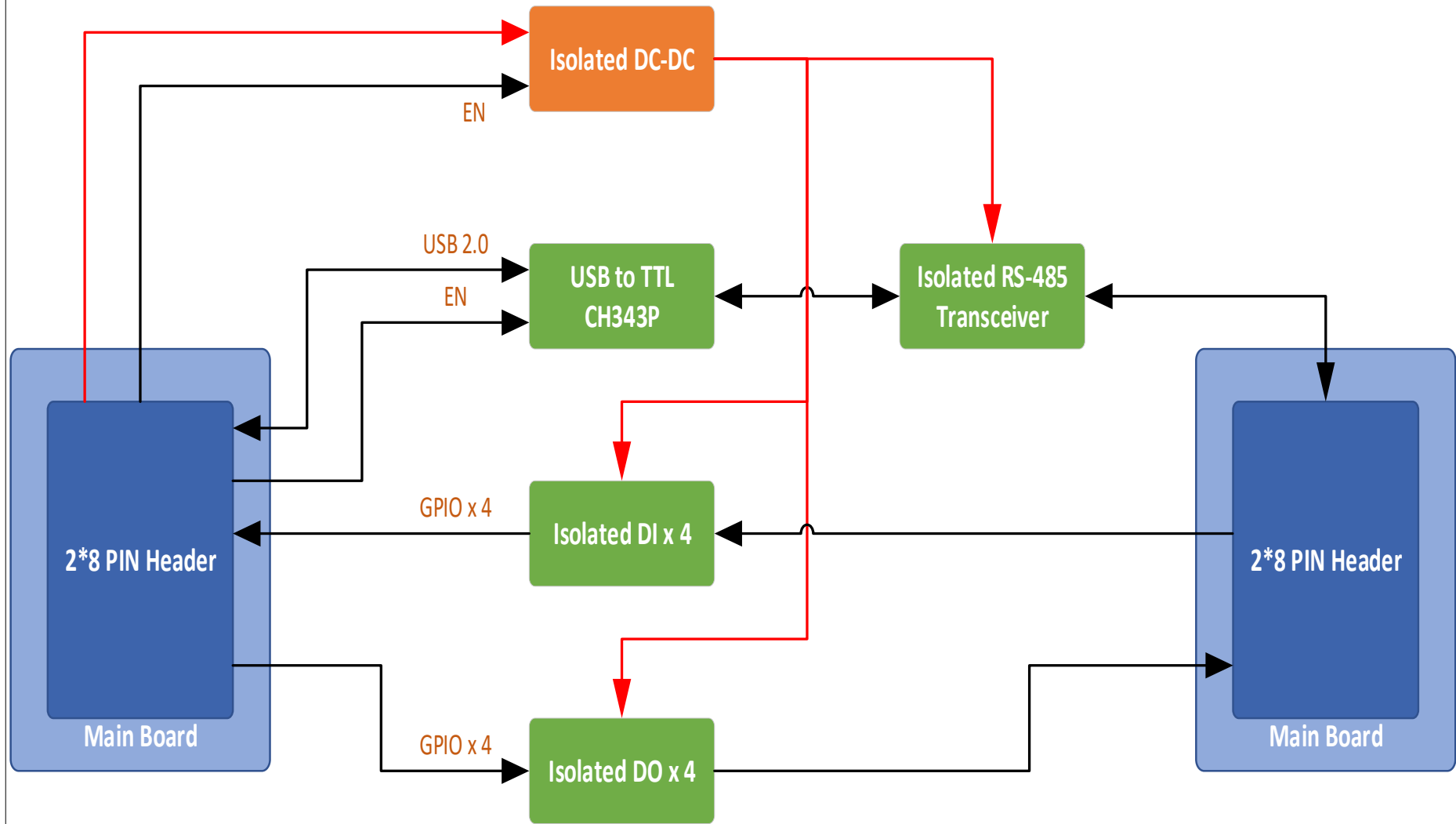
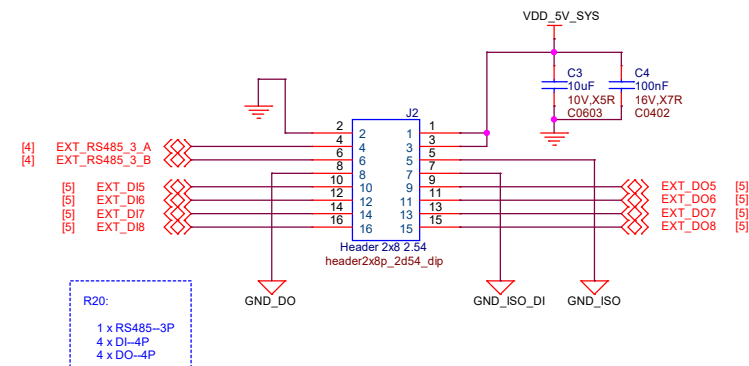
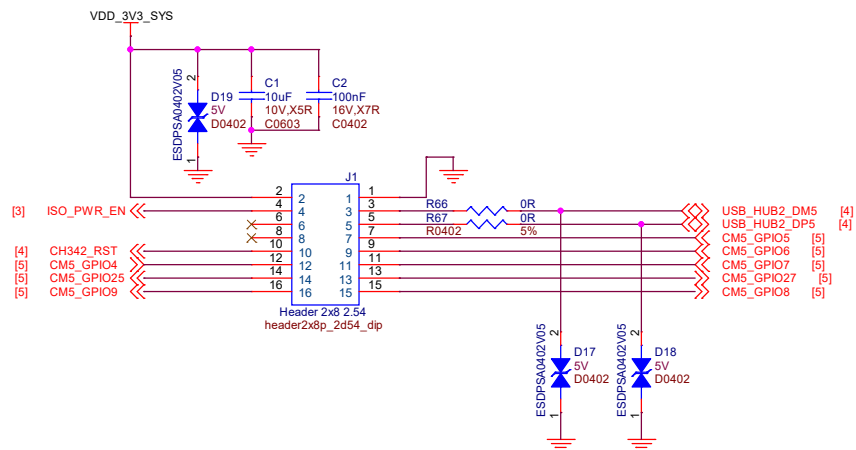


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REVISION HISTORY			
VER #	DATE	DESCRIPTION OF CHANGES	AUTHOR
V1.0	2025.05.27	First release	Linus.Liao

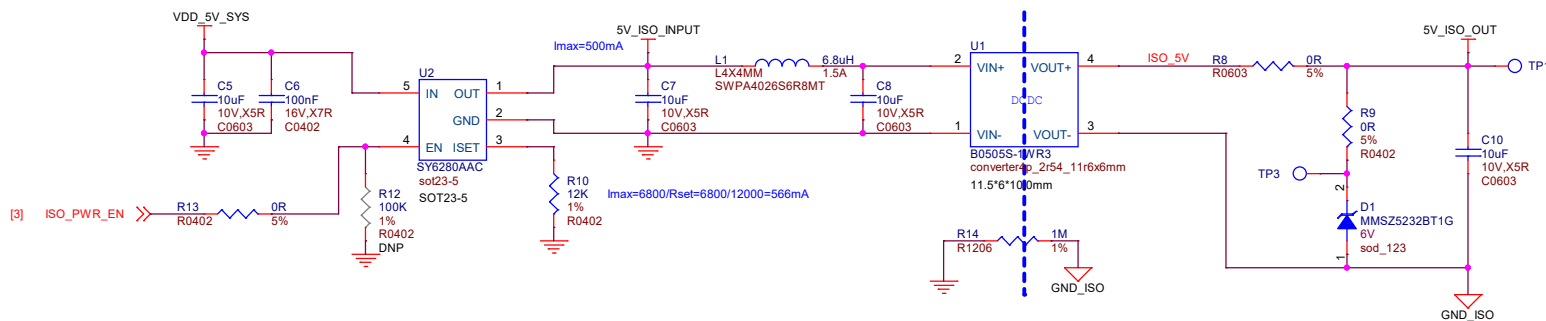
# Block Diagram



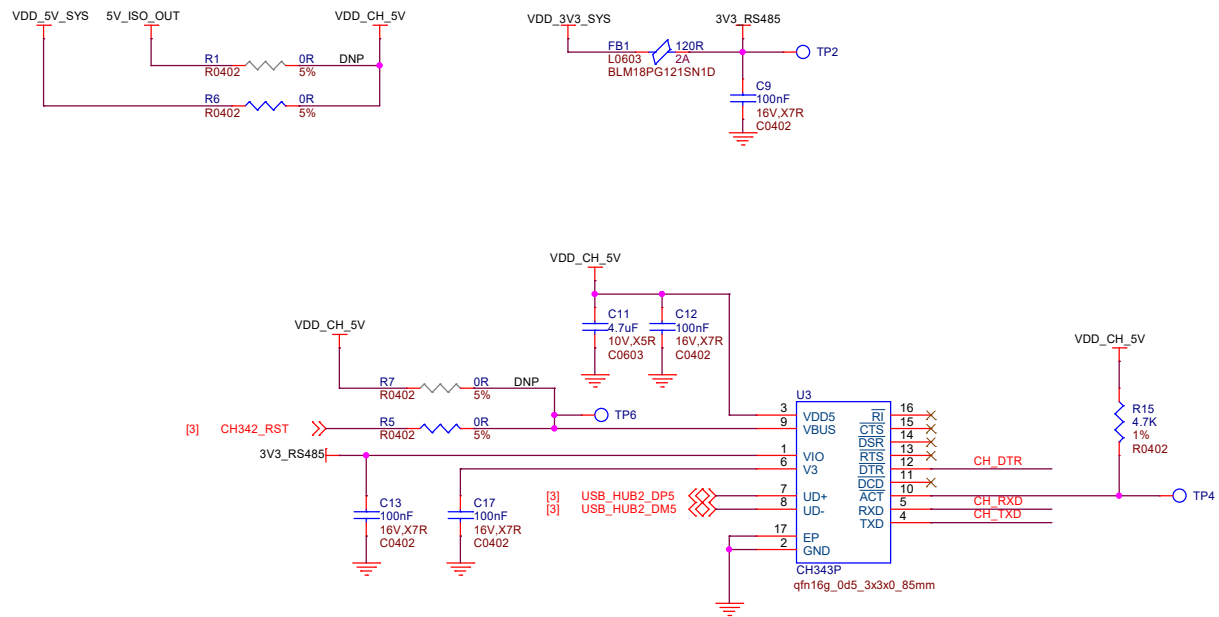


Design Note:

DI: DI\_5: CM5\_GPIO4  
DI\_6: CM5\_GPIO5  
DI\_7: CM5\_GPIO6  
DI\_8: CM5\_GPIO7  
DO: DO\_5: CM5\_GPIO8  
DO\_6: CM5\_GPIO9  
DO\_8: CM5\_GPIO27  
DO\_7: CM5\_GPIO25



## Isolated DCDC 5V to 5V



## RS485

